

PhD studentship (Full-time)

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| Institution | Xi'an Jiaotong-Liverpool University, China |
| School | School of Advanced Technology |
| Supervisors | Principal supervisor: Dr. Miao Cui (XJTLU) Co-supervisor: Dr. Xiaoxu Guo (JITRI) Co-supervisor: Dr. Chun Zhao (XJTLU) Co-supervisor: Dr. Harm Van Zalinge (UoL) |
| Application Deadline | Open until the position is filled |
| Funding Availability | Funded PhD project |
| Project Title | High Performance Analog and RF IC Design Technology Research |
| Contact | Please email Miao.Cui02@xjtlu.edu.cn (XJTLU principal supervisor's email address) or guoxx@jsic-tech.com (JITRI supervisor's email) with a subject line of the PhD project title |

Requirements:

The candidate should have a first class or upper second class honours degree, or a master's degree (or equivalent qualification) in Microelectronics and Solid State Electronics, Circuit and System, Electronic Science and Technology. Solid background in RF microwave circuits and advanced analog circuits; familiar with semiconductor devices and IC fabrication technology; familiar with Analog EDA design tools and Linux systems.

Evidence of good spoken and written English is essential. The candidate should have an IELTS score of 6.5 or above, if the first language is not English. This position is open to all qualified candidates irrespective of nationality.

Please note that the joint PhD project is industry-based and the candidate is expected to undertake part of the research at the partner organization in China.

Degree:

The student will be awarded a PhD degree from the University of Liverpool (UK) upon successful completion of the program.

Funding:

This PhD project is a collaborative research project between XJTLU (<http://www.xjtlu.edu.cn>) in Suzhou and JITRI (Jiangsu Industrial Technology Research Institute) Institute of Intelligent Integrated Circuit Design Technology (智能集成电路设计技术研究所). The student will be registered as an XJTLU PhD student but is expected to carry out the major part of his or her research at the Institute in Institute of Intelligent Integrated Circuit Design Technology (智能集成电路设计技术研究所).

The PhD studentship is available for three years subject to satisfactory progress by the student. The award covers tuition fees for three years (currently equivalent to RMB 80,000 per annum). In addition, during the period of undertaking main research at institute in Suzhou, the PhD candidate will be provided with monthly living allowance at a standard 5000 RMB per month by Institute of Intelligent Integrated Circuit Design Technology (智能集成电路设计技术研究所).

Project Description:

For more information about doctoral scholarship and PhD programme at Xi'an Jiaotong-Liverpool University (XJTLU): Please visit

<http://www.xjtlu.edu.cn/en/study-with-us/admissions/entry-requirements>

<http://www.xjtlu.edu.cn/en/admissions/phd/feescholarships.html>

Supervisor Profile:

Principal Supervisor:

Dr. Miao Cui received PhD degree from University of Liverpool, and M.S. degree from Chinese Academy of Sciences (SINANO), and B.S. degree from Sichuan University. She has research and work experience in microelectronics for more than 10 years, including CMOS technology, GaN LEDs, GaN power devices and integrated circuits (ICs). Her current research interests are GaN power devices and power ICs.

<https://www.xjtlu.edu.cn/zh/study/departments/academic-departments/electrical-and-electronic-engineering/department-staff/academic-staff/staff/miao-cui02>

JITRI co-supervisor:

Dr. Xiaoxu Guo received PhD degree from Institute of Microelectronics, Chinese Academy of Science, M.S. degree from Chinese Academy of Sciences, B.S. degree

from Lanzhou University. Dr. Guo has IC design and industry experience for more than 20 years. He is a specialist in high-speed mixed-signal circuit design, his current research focuses on high-speed ADC circuit design, multi-channel ADC, high-precision low-power ADC and sub-GHz RF circuit design.

How to Apply:

Interested applicants are advised to email Miao.Cui02@xjtlu.edu.cn (XJTLU principal supervisor's email address) or guoxx@jsic-tech.com (JITRI supervisor's email) the following documents for initial review and assessment (please put the project title in the subject line).

- CV
- Two reference letters with company/university letterhead
- Personal statement outlining your interest in the position
- Proof of English language proficiency (an IELTS score of 6.5 or above)
- Verified school transcripts in both Chinese and English (for international students, only the English version is required)
- Verified certificates of education qualifications in both Chinese and English (for international students, only the English version is required)
- PDF copy of Master Degree dissertation (or an equivalent writing sample) and examiners reports available